

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION  
ENGINEERING**

**KONGU ENGINEERING COLLEGE**

(Autonomous)

**PERUNDURAI ERODE – 638060**


**MAY 2022**

**BONAFIDE CERTIFICATE**

This is to certify that the Project Work II Report entitled **DESIGN OF VLSI ARCHITECTURE FOR DIGITAL PLL USING SYNCHRONOUS BINARY COUNTERS** is the bonafide record of project work done by **SOVIYA S (18ECR206)**, **SNEHA S (18ECR205)** and **SARAN L (18ECL253)** in partial fulfilment of the requirements for the award of the Degree of Bachelor of Engineering in Electronics and Communication Engineering of Anna University, Chennai during the year 2021 - 2022.

  
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**INTERNAL EXAMINER**

  
**EXTERNAL EXAMINER**