

KONGU ENGINEERING COLLEGE

DEPARTMENT OF EIE

VALUE ADDED COURSE ON FPGA Based System Design

Circular and Schedule

21.09.2021

It is planned to conduct a value added course titled "FPGA Based System Design" For the III year students through online mode for a duration of 30 hours. The course deals with design of simple logics in hardware description languages, schematic level in Xilinx and ALTERA. Interested Students can enroll their names to the coordinator. *Course Commence on 23.09.21.*

1. Dr.T.Kalavathi Devi
 2. Dr.M.MadhanMohan
 3. Dr.K.N.Baluprithviraj
- Associate Professor(s)
Department of EIE

Schedule

S.No	Day 1 & Timings 3.30pm to 5.30pm	Topic
1	Day1	Introduction to HDL
2	Day 2	Programming concepts in VHDL and verilog
3	Day 3	Levels of abstraction
4	Day 4	CPLDs and FPGAs
5	Day 5	Xilinx System design
6	Day 6	Simulation In Xilinx
7	Day 7	Implementation of FSM in FPGA
8	Day 8	Schematic level design
9	Day 9	Quartus – Simulator
10	Day 10	Design and simulation of hardware models
11	Day 11	DE_2 board implementation
12	Day 12	Interfacing of peripherals
13	Day 13	FPGA architecture
14	Day 14	FPGA implementation
15	Day 15	Interview and technical questions

Phani
21/9/2021
Coordinator

huh
HoD(EIE) 21/9/21

KONGU ENGINEERING COLLEGE, PERUNDURAI-638060

DEPARTMENT OF EIE

PROGRAM APPROVAL (AY: 2021-2022)

Ref. No: <u>KEC/EIE/VAC/2021-22/03</u>		Date: <u>21.09.2021</u>
Type of the Program (Seminar/ Workshop/VAC/ One credit course and etc): <u>VAC - FPGA Based System Design</u>		
No. of days:	Duration: From <u>23.07.21</u> to <u>15.10.21</u>	
Number of Participants	<u>25</u>	
Highlights:		
1. It deals with the design logics, system in FPGA. 2. Imparts knowledge on Xilinx, ALTERA		
Invited Resource Persons: (with address)		
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Coordinators:		
1. Dr. T. Kalwathi Devi 2. Dr. M. Madhavi Mohan 3. Dr. K. N. Balu Pothviraj		

Done 21/9/21
 21/9/21
 Coordinator (S)
 Done 21/9/21

21/9/21
Faculty in-charge

[Signature]
HOD (EIE)



Department of EEE
Attendance Sheet

Title of the Course: FPGA Based System Design
Date: From 23.09.2021 to 01.11.2021

S.No.	Name of the Participants	23.09.21	24.09.21	25.09.21	27.09.21	28.09.21	29.09.21	30.09.21	01.10.21	04.10.21	05.10.21	07.10.21	08.10.21	11.10.21	12.10.21	01.11.21
1.	ANUJA E	/	/	/	/	/	2	/	/	/	/	/	/	/	/	/
2.	ATHIF AHAMED M	/	/	/	/	/	/	/	/	/	/	/	2	/	/	/
3.	CHARUMITHRA I	/	/	2	7	/	/	/	/	/	/	/	/	/	/	/
4.	DHANA LAKSHMI M	/	7	/	/	/	/	/	/	/	/	/	/	/	/	/
5.	DHAYANIDHI R G	/	/	/	/	/	/	/	/	/	/	/	/	9	/	/
6.	DHIVYA SHREE A	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/
7.	ELAKHIYA K B	/	/	2	/	/	/	/	/	/	/	/	/	/	/	/
8.	ELANGO P	/	/	/	/	2	/	/	/	/	/	/	/	/	/	/
9.	GEERTHIGA T	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/
10.	GOKUL U	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/

17/11/21

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News 13 11/24
Coordinator

HoD (EIE)



KONGU ENGINEERING COLLEGE
(Autonomous)
PERUNDURAI ERODE - 638068, TAMILNADU, INDIA
DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION ENGINEERING



Perundurai

CERTIFICATE

This is to certify that Mr. Ms. **ATHIF AHAMED M** has attended the value added course on **FTGA Based System Design** from **22.09.2021 to 01.11.2021** (30 Hours) conducted at Kongu Engineering College, Perundurai, Erode

P. K. S.

Co-ordinator

[Signature]

HOD

