

UP TO
₹. 20,000/-

KONGU ENGINEERING COLLEGE, PERUNDURAI, ERODE-638 060
PROGRAMME APPROVAL FORM

AD-12-PP
REV.1
01.01.2021

Proposal Tracking No. Ref: EIR/2020-2021/03/Dr. T. Kalavathi devi/0104

Dept. Ref. No.: EI/2020-2021/017

Date: 27.3.21

1.	Department:	EIE		
2.	a). Name of the Programme	Mixed Signal Circuit: Technology, Logic Design and CAD Tools		
	b). Norms under which it is proposed	Value Added Course (Online mode)		
3.	Dates:	From: 29.3.21	TO: 18.3.21	No. of Days 15
4.	Programme for:	Faculty	Students	Others (Specify)
5.	Level:	State / National / International	Association	Institution / Dept. / Cell / General
6.	Highlights of the Programme:	Value added course to enrich the Simulation knowledge in CAD Tools		

1. Proposed Invitees:

S No.	Purpose	Chief Guest	Speakers / Jury	Trust Member Invitees
1	-			
2				

2. Agenda: Programme Agenda / Schedule Enclosed : Yes / No

3. Expenditure:

S.No.	Purpose	Proposed	Revised	Actual	Remarks
1	E-certificate	-			
2					
3					
4					
5					
6					
Total		-	N/A		

4. Source

		Proposed	Actual
Budget Allotment	-	Sponsors -	
Balance Amt. Available	-	KEC -	
Expenses Proposed	-	Registration -	
Balance	-		

Plakke 27/3/21
Faculty i/c (with Name)
(T. Kalavathi devi)

H.O.D

CCO

PRINCIPAL

Kongu Engineering College, Perundurai

Department of EIE

Circular

26.03.2021

It is planned to organize a value added course to enrich the simulation knowledge in CAD tools on the title **Mixed Signal Circuit: Technology, Logic Design and CAD Tools** for the II year students.

Course Coordinators:

Dr.T.Kalavathi Devi

Dr.M.Madhan Mohan

Dr.K.N.Baluprithviraj

Associate Professor(s),EIE

It is organized through online mode from 29.03.2021 to 13.04.2021, Timings: 3.30 pm to 5.30 pm with the proposed schedule as below:

S.No.	Scheduled Days	Topics
1.	Day1	Design of Analog circuits- Opamp in SPICE
2.	Day2	Arithmetic circuits
3.	Day3	Amplifiers
4.	Day4	Wave shaping circuits
5.	Day5	Anadigm – FPAA
6.	Day6	circuit techniques for low-voltage and high-speed A/D converters
7.	Day7	Design of Digital Circuits- Microelectronic Circuits
8.	Day8	Architecture level
9.	Day9	Counters, Storage cells
10.	Day10	Sub block creation
11.	Day11	Interfacing circuits – PWM
12.	Day12	Controlling circuits – SCR,TVS
13.	Day13	Motor interfacing – stepper, wiper, DC
14.	Day14	VLABs – TINA
15.	Day15	PCB design and Fabrication

P. K. Kalavathi
26/3/21
Course Coordinator

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27/3/21
HoD(EIE)

Non formal course or value added course

1. Mixed Signal circuit logic design, Technology and CAD tools - 29.03.2021 to 17.04.2021

S.No.	Name of the Participants
1.	Dhayashree .P 19EIR015
2.	Divyashree A 19EIR017
3.	Geerthiga T 19EIR021
4.	Gobeswar P 19EIR023
5.	Kanchiverma V 19EIR034
6.	Keerthana P 19EIR038
7.	Lakshmiprabha G 19EIR042
8.	Murugan S 19EIR051
9.	Muthukumarasamy M 19EIR052
10.	Narendh N 19EIR054
11.	Paveen Kumar D 19EIR056
12.	Pavithra Devi S 19EIR057
13.	Priyadharshini R 19EIR062
14.	Priyanga M 19EIR063
15.	Rithanya S 19EIR066
16.	Roshini R 19EIR067
17.	Rupasri M 19EIR068
18.	Sai parisith Adithya M 19EIR069
19.	Sakthivel M 19EIR070
20.	Sanjai S 19EIR071
21.	Sanjana G 19EIR072
22.	Sarankumar K S 19EIR075
23.	Shalini V 19EIR079
24.	Sharanya T 19EIR080
25.	Sedhu M 19EIR082
26.	Shivanandam S 19EIR083
27.	Shreejith A 19EIR084
28.	Slvasanthosh K 19EIR085
29.	Srinithi K 19EIR088

Non formal course or value added course

1. Mixed Signal circuit logic design, Technology and CAD tools - 29.03.2021 to 17.04.2021

30.	Subikhshaa R	19EIR089
31.	Suryapraha V L	19EIR091
32.	VARSINISHRILAYA P K	19EIR092
33.	Varuna S	19EIR093
34.	Vikashini S N	19EIR095
35.	Dharunkumar G	19EIR014
36.	Elakiya K B	19EIR018
37.	Gokul U	19EIR024
38.	Kabin K	19EIR032
39.	Kavinkumar N	19EIR036
40.	Kowshika B	19EIR041
41.	Loahit S	19EIR043
42.	Aruna G	19EIR004
43.	Athif Ahmed M	19EIR005
44.	Hariprasath A	19EIR027
45.	Srivadani M	19EIR086